**CHAPTER 4**

**FUTURE SCOPE AND CONCLUSION**

**Future Scope:**

a) In this project we did the frequency and duty cycle variation by integer factor.

So, we can extend our code with the same logic for fractional division and

multiplication of input clock.

b) These various clocks can be used in devices that are used everyday that includes

the use of clocks such as computer, mobile phones, alarms, stop watches etc.

c) Clock implementation is required in all these machines for standard display

of time.

**Conclusion:**

In this phase of project the required study of algorithm of Multiplication/

Division and Duty Variation has been performed and development of frequency division has been done. Results are verified by simulation.